



SEMITRANS® 2

High Speed IGBT4 Modules

SKM100GB12F4

Features*

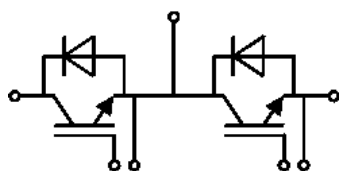
- High speed trench and field-stop IGBT
- CAL4 ultra-fast = soft switching 4. generation CAL-diode
- Insulated copper baseplate using DBC technology (Direct Bonded Copper)
- Increased power cycling capability
- For higher switching frequencies above 15kHz
- UL recognized, file no. E63532

Typical Applications

- UPS
- Electronic welders
- Inductive heating
- Switched mode power supplies

Remarks

- Case temperature limited to $T_c = 125^\circ\text{C}$ max.
- Recommended $T_{op} = -40 \dots +150^\circ\text{C}$
- Product reliability results valid for $T_j = 150^\circ\text{C}$



GB

Absolute Maximum Ratings

Symbol	Conditions		Values	Unit
IGBT				
V _{CES}	T _j = 25 °C		1200	V
I _C	T _j = 175 °C	T _c = 25 °C	153	A
		T _c = 80 °C	117	A
I _{Cnom}			100	A
I _{CRM}	I _{CRM} = 2 x I _{Cnom}		200	A
V _{GES}			-20 ... 20	V
t _{psc}	V _{CC} = 800 V V _{GE} ≤ 15 V V _{CES} ≤ 1200 V	T _j = 150 °C	10	μs
T _j			-40 ... 175	°C
Inverse diode				
V _{RRM}	T _j = 25 °C		1200	V
I _F	T _j = 175 °C	T _c = 25 °C	111	A
		T _c = 80 °C	82	A
I _{Fnom}			100	A
I _{FRM}	I _{FRM} = 2xI _{Fnom}		200	A
I _{FSM}	t _p = 10 ms, sin 180°, T _j = 25 °C		550	A
T _j			-40 ... 175	°C
Module				
I _{t(RMS)}			200	A
T _{stg}	module without TIM		-40 ... 125	°C
V _{isol}	AC sinus 50 Hz, t = 1 min		4000	V

Characteristics

Symbol	Conditions	min.	typ.	max.	Unit
IGBT					
$V_{CE(sat)}$	$I_C = 100\text{ A}$ $V_{GE} = 15\text{ V}$ chipelevel	$T_j = 25^\circ\text{C}$	2.05	2.38	V
		$T_j = 150^\circ\text{C}$	2.55	2.93	V
V_{CE0}	chipelevel	$T_j = 25^\circ\text{C}$	1.10	1.28	V
		$T_j = 150^\circ\text{C}$	0.95	1.13	V
r_{CE}	$V_{GE} = 15\text{ V}$ chipelevel	$T_j = 25^\circ\text{C}$	9.5	11	$\text{m}\Omega$
		$T_j = 150^\circ\text{C}$	16	18	$\text{m}\Omega$
$V_{GE(th)}$	$V_{GE} = V_{CE}$, $I_C = 3.8\text{ mA}$	5.1	5.8	6.4	V
I_{CES}	$V_{GE} = 0\text{ V}$, $V_{CE} = 1200\text{ V}$, $T_j = 25^\circ\text{C}$			1	mA
C_{ies}	$V_{CE} = 25\text{ V}$ $V_{GE} = 0\text{ V}$	$f = 1\text{ MHz}$	6.2		nF
C_{oes}		$f = 1\text{ MHz}$	0.41		nF
C_{res}		$f = 1\text{ MHz}$	0.35		nF
Q_G	$V_{GE} = -8\text{ V} \dots +15\text{ V}$		567		nC
R_{Gint}	$T_j = 25^\circ\text{C}$		0		Ω
$t_{d(on)}$	$V_{CC} = 600\text{ V}$	$T_j = 150^\circ\text{C}$	12		ns
t_r	$I_C = 100\text{ A}$	$T_j = 150^\circ\text{C}$	20		ns
E_{on}	$V_{GE} = +15/-15\text{ V}$	$T_j = 150^\circ\text{C}$	6.6		mJ
$t_{d(off)}$	$R_{G on} = 3.9\ \Omega$	$T_j = 150^\circ\text{C}$	315		ns
t_f	$R_{G off} = 3.9\ \Omega$	$T_j = 150^\circ\text{C}$	65		ns
E_{off}	$di/dt_{on} = 5000\text{ A}/\mu\text{s}$ $di/dt_{off} = 1300\text{ A}/\mu\text{s}$ $dv/dt = 4300\text{ V}/\mu\text{s}$ $L_s = 26\text{ nH}$	$T_j = 150^\circ\text{C}$	8		mJ
$R_{th(j-c)}$	per IGBT			0.238	K/W
$R_{th(c-s)}$	per IGBT ($\lambda_{grease} = 0.81\text{ W}/(\text{m}^2\text{K})$)		0.122		K/W



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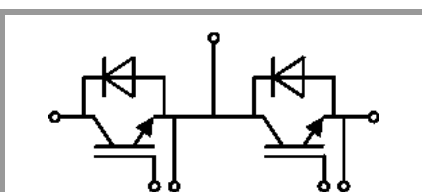
Typical Applications

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Remarks

- Case temperature limited to $T_c = 125^\circ\text{C}$ max.
- Recommended $T_{op} = -40 \dots +150^\circ\text{C}$
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Characteristics						
Symbol	Conditions		min.	typ.	max.	Unit
Inverse diode						
V _F = V _{EC}	I _F = 100 A	T _j = 25 °C		2.55	2.93	V
	V _{GE} = 0 V	T _j = 150 °C		2.46	2.80	V
	chiplevel					
V _{F0}		T _j = 25 °C		1.51	1.75	V
	chiplevel	T _j = 150 °C		1.16	1.40	V
r _F		T _j = 25 °C		10	12	mΩ
	chiplevel	T _j = 150 °C		13	14	mΩ
I _{RRM}	I _F = 100 A	T _j = 150 °C		200		A
Q _{rr}	di/dt _{off} = 5000 A/μs	T _j = 150 °C		16.5		μC
E _{rr}	V _{GE} = -15 V	T _j = 150 °C		6.3		mJ
	V _{CC} = 600 V					
R _{th(j-c)}	per diode				0.483	K/W
R _{th(c-s)}	per diode (λ _{grease} =0.81 W/(m*K))			0.134		K/W
Module						
L _{CE}				30		nH
R _{CC'+EE'}	measured per	T _C = 25 °C		0.65		mΩ
	switch	T _C = 125 °C		1.09		mΩ
R _{th(c-s)1}	calculated without thermal coupling			0.0319		K/W
R _{th(c-s)2}	including thermal coupling, T _s underneath module (λ _{grease} =0.81 W/(m*K))			0.050		K/W
M _s	to heat sink M6		3		5	Nm
M _t		to terminals M5	2.5		5	Nm
				-		Nm
w					160	g



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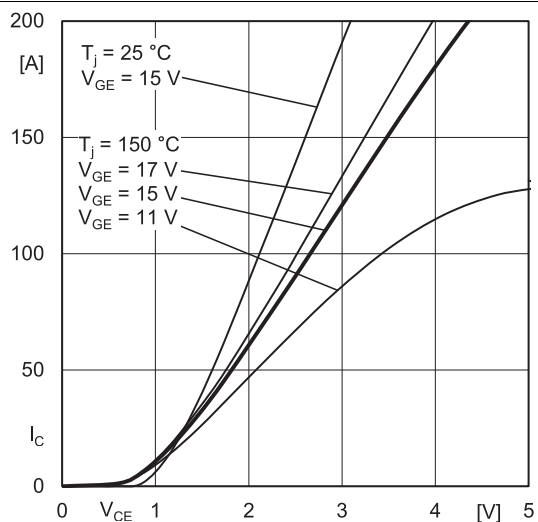


Fig. 1: Typ. output characteristic, inclusive $R_{CC} + EE'$

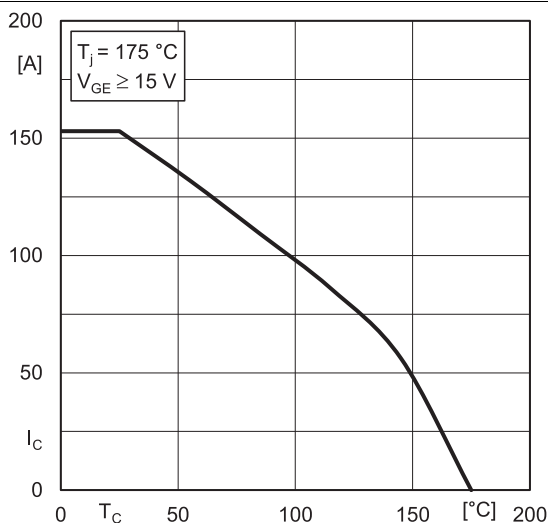


Fig. 2: Rated current vs. temperature $I_C = f(T_C)$

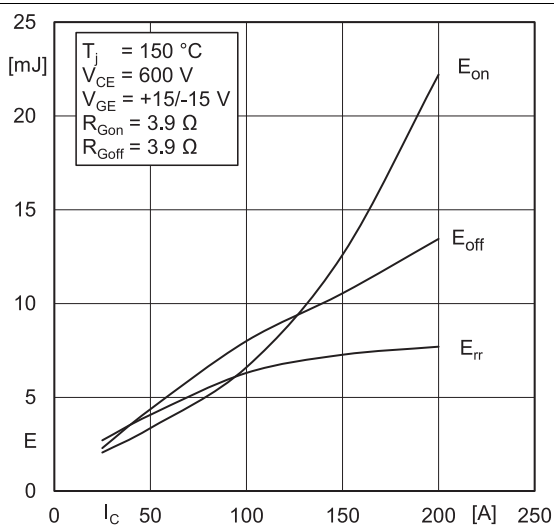


Fig. 3: Typ. turn-on /-off energy = $f(I_C)$

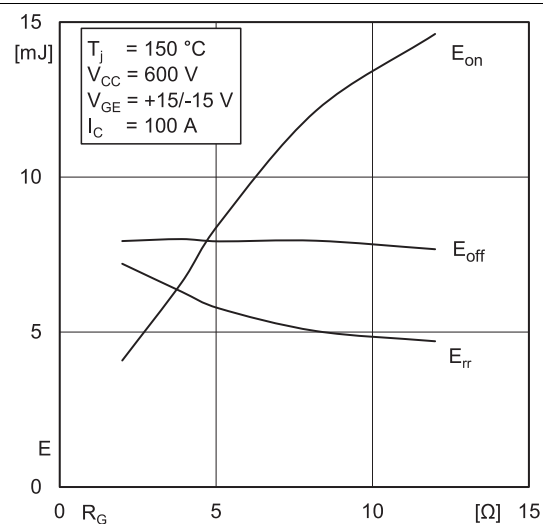


Fig. 4: Typ. turn-on /-off energy = $f(R_G)$

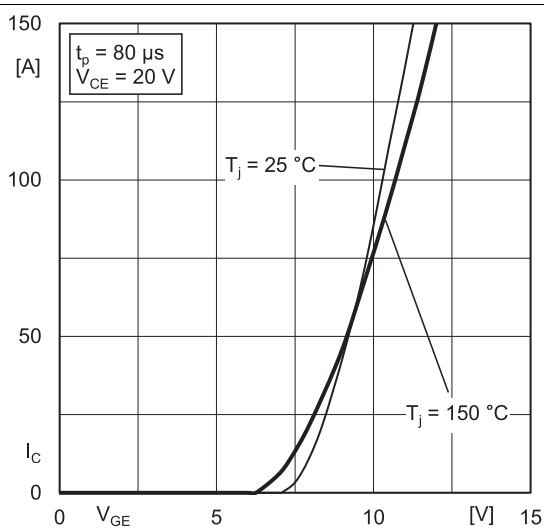


Fig. 5: Typ. transfer characteristic

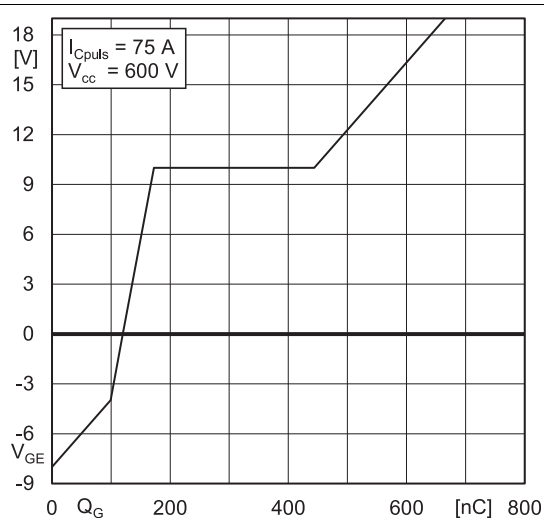
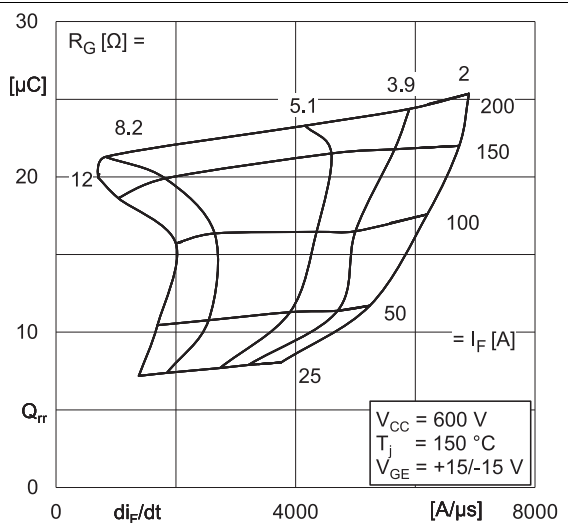
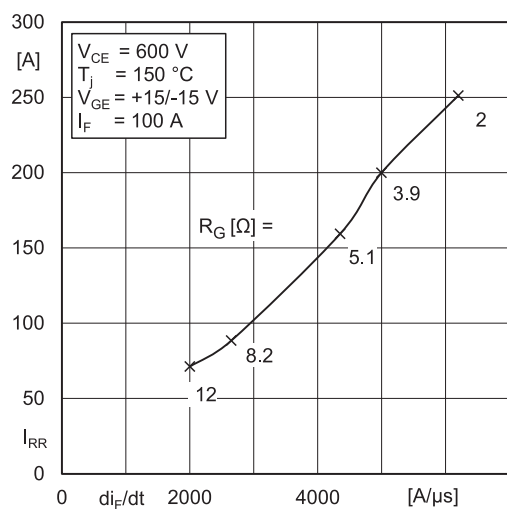
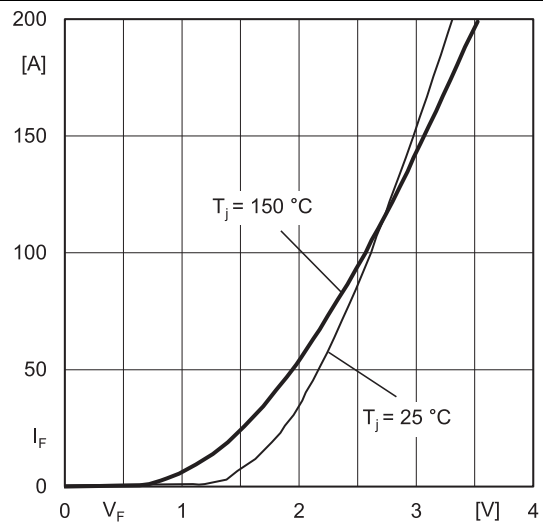
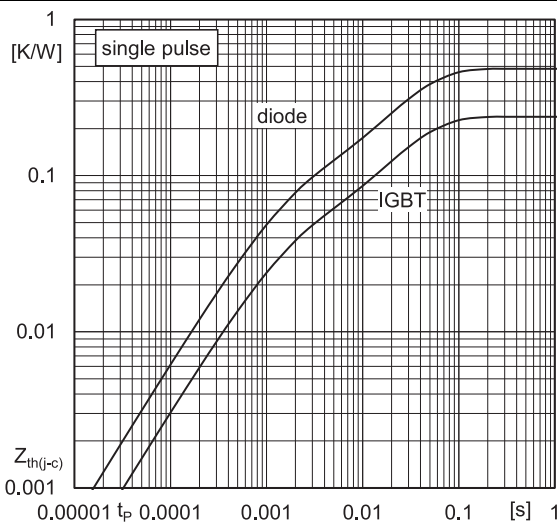
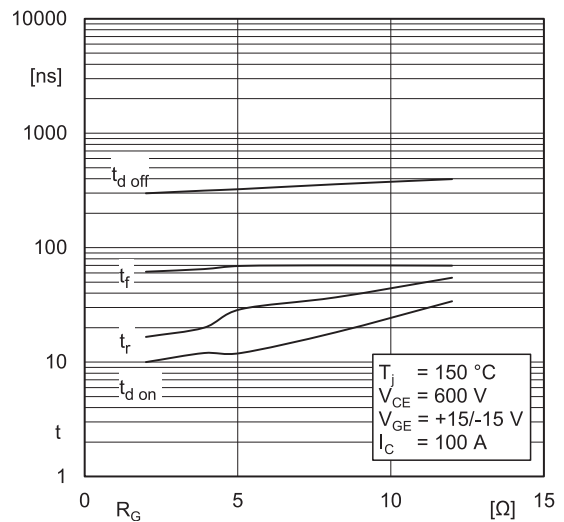
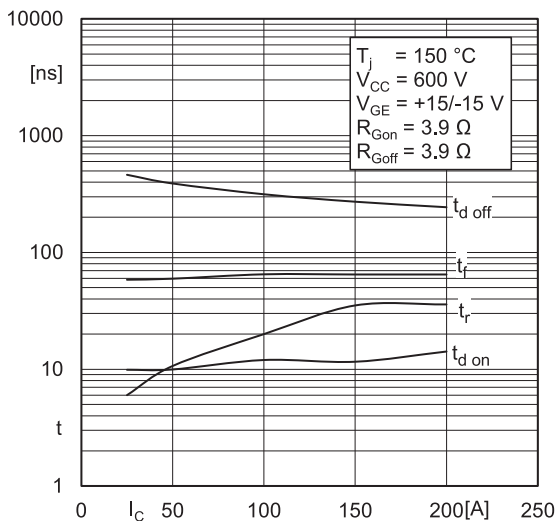


Fig. 6: Typ. gate charge characteristic



max. 7mm

2,8x0,5

23±0,3

23±0,3

5,5

29,5^{+0,5}_{-0,2}

22,1±0,5

5,3^{+0,1}_{-0,2}

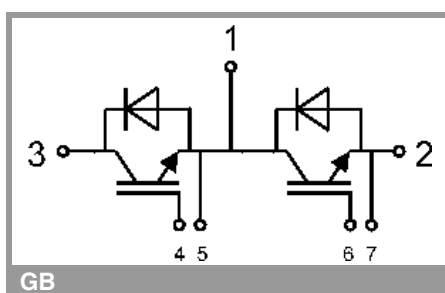
28,9±0,5

30,1±0,5

Technical drawing of a 5-pin DIN connector. The drawing shows the front and side views with the following dimensions and labels:

- Overall Dimensions:**
 - Overall width: 94
 - Overall height: 34
 - Distance between mounting holes: 80 ± 0,4
- Pin Dimensions and Spacing:**
 - Pin 1 diameter: $\phi 6,4$
 - Pin 1 to Pin 2 distance: 16,6 ± 0,4
 - Pin 2 to Pin 3 distance: 26 ± 0,4
 - Pin 3 to Pin 4 distance: 17
 - Pin 4 to Pin 5 distance: 6
 - Pin 5 to Pin 6 distance: 6
 - Pin 6 to Pin 7 distance: 17
 - Pin 7 to Pin 8 distance: 6
 - Pin 8 to Pin 9 distance: 17
 - Pin 9 to Pin 10 distance: 6
 - Pin 10 to Pin 11 distance: 17
 - Pin 11 to Pin 12 distance: 6
 - Pin 12 to Pin 13 distance: 17
 - Pin 13 to Pin 14 distance: 6
 - Pin 14 to Pin 15 distance: 17
 - Pin 15 to Pin 16 distance: 6
 - Pin 16 to Pin 17 distance: 17
 - Pin 17 to Pin 18 distance: 6
 - Pin 18 to Pin 19 distance: 17
 - Pin 19 to Pin 20 distance: 6
 - Pin 20 to Pin 21 distance: 17
 - Pin 21 to Pin 22 distance: 6
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 - Pin 24 to Pin 25 distance: 17
 - Pin 25 to Pin 26 distance: 6
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 - Pin 191 to Pin 192 distance: 6
 - Pin 192 to Pin 193 distance: 17
 - Pin 193 to Pin 194 distance:

SEMITRANS 2



This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

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